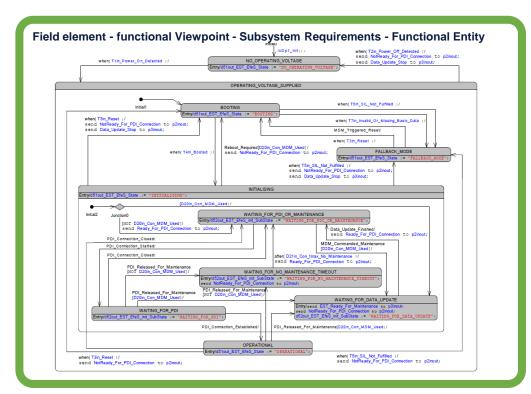


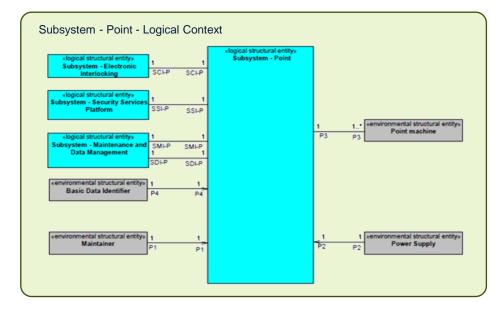
Task 2: Trackside Assets CS



The Trackside CS domain is responsible for the specification of the sub systems controlling the trackside objects like points, track vacancy detection, level crossings and others. With this a unified interface between the Traffic CS system and the object controller ensures the interchangeability between both components.

After the update of specifications in June 2024 (BL4 Release 3), the work in this period should focus on the remaining topics for a final stable release of the specification for Trackside Assets.





The Task 2 Trackside Assets CS Team will focus on developing the following new functions and technical enablers for the **"Harmonized European Rail Operation" migration plateau**:

- EULYNX interfaces and subsystem specifications for the trackside object controllers and multi-element controllers
- Diagnostic, maintenance and security interfaces for trackside assets
- Power management optimization functions
- All other interfaces of the System Pillar Target Architecture, which are agreed and published at this time, can be used for optional implementations



Task 2: Trackside Assets CS

Deliverables Request for Service (SC2.4) – Year 3 [Oct-24 – Oct-25]

D03 Power supply - Feb 2025

D01 Finalize TACS specification - Q3 2025

D02 Interface to field devices – Feb 2025

Lead STIP Deliverables

- STIP_85: Req. Spec. and SCI Interf. Spec. for subsyst. Light Signal -2024
- STIP_86: Req. Spec. and SCI Interf. Spec. for subsyst. Point 2024
- STIP_87: Reg. Spec. and SCI Interf. Spec. for subsyst. Generic IO 2024
- STIP_88: Req. Spec. and SCI Interf. Spec. for subsyst. TDS 2024
- STIP_89: Reg. Spec. and SCI Interf. Spec. for subsyst. LC- 2024
- STIP_90: Interf. Spec. SDI-LS 2024
- STIP_91: Interf. Spec. SDI-P 2024
- STIP_92: Interf. Spec. SDI-IO 2024
- STIP_93: Interf. Spec. SDI-TDS 2024

Latest Achievements, Challenges and Design Decisions (to be filled periodically by the domain)

- Latest Achievements: The following achievements have been accomplished by the TACS domain:
 - Achievement #1: Publication TACS/EULYNX BL4 Release 3 June 2024

Lead STIP Deliverables

- STIP_94: Interf. Spec. SDI-LC 2024
- STIP 95: Spec. of Standardized Diagnostics Interface (TACS): Interface definition and specification SDI Generic - 2024
- STIP_96: Spec. of Standardized Maintenance Interface (TACS): Interface definition and specification and subsyst. reg. for SMI - 2024
- STIP_97: Spec.of Maintenance and data management (TACS) 2024
- STIP_98: Specification of Point of Service-Signalling 2024
- STIP_99: Interface Def., Spec. and subsyst. req. for SCI Generic 2024
- STIP_100: Generic interface and subsyst. req. 2024

Expected outcomes for sector review in the next 3 months

Decision proposals

- for further harmonisation of interfaces to field devices
- for harmonisation of interfaces for power supply and power management