



**EULYNX Initiative**



**Europe's Rail Joint Undertaking**

## **Generic interface and subsystem requirements**

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Generic interface and subsystem requirements						
ID	Type	Requirement Part 1		Requirement Part 2	JIRA	V 4.0 (6.A) > V 4.0 (4.A)
Eu.Gen.1	Head	1 Introduction				
Eu.Gen.5	Head	1.1 Release information				
Eu.Gen.6	Info	[Eu.Doc.20] EULYNX Generic interface and subsystem requirements CENELEC Phase: 4 Version: 4.0 (6.A) Approval date: 29.05.2024				<b>Object Text:</b> [Eu.Doc.20] EULYNX Generic interface and subsystem requirements CENELEC Phase: 4 Version: 4.0 ( <del>4</del> 6.A) Approval date: <a href="#">29.05.2024</a>
Eu.Gen.7	Info	Version history				
Eu.Gen.4249	Info	version number: 4.0 (0.A) date: 16.05.2022 author: Dennis Kunz, Filip Giering generic profile version: 18 review: CCB changes: EUAR-508, EUAR-510, EUAR-523, EUAR-524, EUAR-526				
Eu.Gen.4251	Info	version number: 4.0 (1.A) date: 31.03.2023 author: Filip Giering generic profile version: 21 review: changes: EUAR-564, EUAR-572, EUAR-576				
Eu.Gen.4254	Info	version number: 4.0 (2.A) date: 11.05.2023 author: Filip Giering model version: 22 review: cluster changes: EUAR-589, EUAR-593				
Eu.Gen.4256	Info	version number: 4.0 (3.A) date: 27.06.2023 author: Filip Giering model version: 22 review: TACS Mirror Group changes: EUAR-594, EUAR-612, EUAR-613				
Eu.Gen.4257	Info	version number: 4.0 (4.A) date: 15.12.2023 author: Filip Giering model version: 25 review: M&T changes: EUAR-550, EUAR-660, EUAR-662, EUAR-663, EUAR-664, EUAR-665, EUAR-667, EUAR-675				
Eu.Gen.4262	Info	version number: 4.0 (5.A) date: 21.03.2024 author: Philipp Wolber model version: 26 review: cluster changes: EUAR-638				object created after baseline 4.0 (4.A)
Eu.Gen.4263	Info	version number: 4.0 (6.A) date: 18.06.2024 author: Philipp Wolber model version: 26 review: TACS Mirror Group changes: EUAR-681, EUAR-708, EUAR-740				object created after baseline 4.0 (4.A)
Eu.Gen.14	Head	1.2 Impressum				
Eu.Gen.15	Info	Publishers:  Europe’s Rail Joint Undertaking <a href="https://rail-research.europa.eu/">https://rail-research.europa.eu/</a>  EULYNX Initiative <a href="https://eulynx.eu/">https://eulynx.eu/</a>			EUAR-681	<b>Object Text:</b> Publishers:  Europe’s Rail Joint Undertaking <a href="https://rail-research.europa.eu/">https://rail-research.europa.eu/</a>  EULYNX Initiative <del>A full list of the EULYNX Partners can be found on</del> <del>www-</del> <a href="https://eulynx.eu/index.php/members">https://eulynx.eu/index.php/members</a> <b>a_JIRA_BL4R3:</b> <a href="#">EUAR-681</a>
Eu.Gen.16	Info	Responsible for this document: EU-Rail System Pillar Trackside Assets Control and Supervision domain				

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Eu.Gen.3	Head	<b>1.3 Purpose</b>			
Eu.Gen.4	Info	The purpose of the document is the specification of generic requirements.			
Eu.Gen.2	Info	This document describes: <ul style="list-style-type: none"><li>generic functional requirements for a EULYNX field element Subsystem</li></ul>			
Eu.Gen.18	Info	This document is intended for the following users: <ul style="list-style-type: none"><li>safety authorities</li><li>infrastructure managers</li><li>safety assessors</li><li>signalling system suppliers</li><li>validators</li></ul>			
Eu.Gen.19	Info	This document is the basis for the implementation by the supplier and for approval by the infrastructure manager.			
Eu.Gen.4255	Info	This document is applicable for both the EU-Rail System Pillar target architecture and the EULYNX architecture. The document is delivered as a single specification fitting both the System Pillar documentation sets and the EULYNX documentation sets. EU-Rail System Pillar is the technical authority for this document.			
Eu.Gen.20	Head	<b>1.4 Applicable standards and regulations</b>			
Eu.Gen.21	Info	A list of applicable standards and regulations used in EULYNX is listed in the EULYNX Reference Document List [Eu.Doc.12].			
Eu.Gen.22	Head	<b>1.5 Applicable documents</b>			
Eu.Gen.23	Info	The current versions of documents used as input or related to this document are listed in the EULYNX Documentation Plan [Eu.Doc.11]. The relationships between the documents are displayed in the Appendix A1 Documentation plan and structure [Eu.Doc.11_A1].			
Eu.Gen.24	Head	<b>1.6 Terms and abbreviations</b>			
Eu.Gen.25	Info	The terms and abbreviations are listed in the EULYNX Glossary [Eu.Doc.9].			
Eu.Gen.376	Head	<b>1.7 Variability management</b>			
Eu.Gen.377	Info	This document describes harmonised requirements. Variability management is not applicable. The specific applicability of requirements is captured in individual interface specifications.			
Eu.Gen.26	Head	<b>1.8 Definition of object types</b>			
Eu.Gen.27	Info	The following definition for object types is applied in this document:			
Eu.Gen.28	Info	<ul style="list-style-type: none"><li>"Req" - This denotes a mandatory requirement.</li></ul>			
Eu.Gen.4259	Info	<ul style="list-style-type: none"><li>"Def" - This denotes referenceable model elements that are used in the model-based creation of requirements</li></ul>			
Eu.Gen.29	Info	<ul style="list-style-type: none"><li>"Info" - This denotes additional information to help understand the specification. These objects do not specify any additional requirements.</li></ul>			
Eu.Gen.30	Info	<ul style="list-style-type: none"><li>"Head" - This denotes chapter headings.</li></ul>			
Eu.Gen.31	Head	<b>1.9 Modelling</b>			
Eu.Gen.32	Info	The section "Generic requirements for subsystems" follows a model based systems engineering process using Systems Modelling Language (SysML) and defines the functional system requirements for the EULYNX field element Subsystem in stimulus-response form.			
Eu.Gen.33	Info	The diagrams presented in this document are modelled in SysML [SysML].			
Eu.Gen.34	Info	The rules for the interpretation of the model based parts of specification are defined in [Eu.Doc.29].			
Eu.Gen.35	Info	In chapter 3 "Generic requirements for subsystems" the functional system requirements, defined in the form of a SysML model in the PTC Integrity Modeler are depicted as a surrogate of this model in the form of DOORS-objects.			
Eu.Gen.36	Info	A requirement thereby consists of the respective SysML model element, for instance a SysML diagram, and if necessary an additional extension of the requirement.			
Eu.Gen.37	Info	In the column "Requirement Part 1" the particular SysML model element is depicted and in the column "Requirement Part 2" the corresponding extension of the definition is given. The stated object type normally applies both to "Requirement Part 1" and to "Requirement Part 2".			

ID	Type	Requirement Part 1	Requirement Part 2	JIRA	V 4.0 (6.A) > V 4.0 (4.A)
Eu.Gen.38	Info	There are requirements with type "Req" given, where the column "Requirement Part 2" or a part of it is provided with the heading "Information". In this case, the defined type only applies to the column "Requirement Part 1" and the part of "Requirement Part 2", which is not labelled as "Information".			
Eu.Gen.4258	Info	State machines or several state machines linked together in a Functional Architecture define the totality of all functional requirements of an SUS or an SIUS in a coherent and consistent manner. State diagrams of a corresponding state machine are marked with the object type "Req". For the later design and implementation, it is not the description language SysML that is binding, but the domain-specific meaning expressed by it. The specified behaviour can be converted into a vendor specific language but must retain the domain specific meaning describing the functional requirements. The specific model elements are additionally specified and defined by object type "Def" to allow for traceability to supplier designs or test cases. The compliance of products to the specifications must be demonstrated by testing against EULYNX test cases, which are derived from the functionality specified by the models.		EUAR-740	<b>Object Text:</b> State machines or several state machines linked together in a Functional Architecture define the totality of all functional requirements of an SUS or an SIUS in a coherent and consistent manner. State diagrams of a corresponding state machine are marked with the object type "Req". For the later design and implementation, it is not the description language SysML that is binding, but the domain-specific meaning expressed by it. The specified <del>behavior</del> <b>behaviour</b> can be converted into a vendor specific language but must retain the domain specific meaning describing the functional requirements. The specific model elements are additionally specified and defined by object type "Def" to allow for traceability to supplier designs or test cases. The compliance of products to the specifications must be demonstrated by testing against EULYNX test cases, which are derived from the functionality specified by the models. <b>a_JIRA_BL4R3:</b> <a href="#">EUAR-740</a>
Eu.Gen.48	Head	<b>2 Conditions of use</b>			
Eu.Gen.369	Info	The specifications defined in this document shall follow the requirements of the EULYNX System Architecture Specification [Eu.Doc.16].			
Eu.Gen.49	Head	<b>3 Generic requirements for subsystems</b>			
Eu.Gen.113	Head	<b>3.1 EULYNX field element Subsystem</b>			
Eu.Gen.162	Head	<b>3.1.1 EULYNX field element Subsystem - General Infos and Assumptions</b>			
Eu.Gen.4198	Head	<b>3.1.1.1 EULYNX field element Subsystem - Internal Information Flows</b>			
Eu.Gen.4199	Def	<div><div>[Package] EULYNX field element Subsystem - Internal Information Flows [Subsystem Requirements - Direction of Information Objects]</div><div><div><b>bdd</b> [Package] EULYNX field element Subsystem - Internal Information Flows [Subsystem Requirements - Direction of Information Objects]</div><div><div><div><div>«information flow» EST_SCI_GEN</div><div>prov «signal» Ready_For_PDI_Connection prov «signal» NotReady_For_PDI_Connection reqd «signal» PDI_Connection_Established reqd «signal» PDI_Connection_Closed reqd «signal» PDI_Released_For_Maintenance reqd «signal» PDI_Connection_Started</div></div><div><div>«information flow» EST_SMI_GEN</div><div>reqd «signal» EST_Ready_For_Maintenance prov «signal» Data_Update_Finished prov «signal» MDM_Commanded_Maintenance prov «signal» Reboot_Required prov «signal» MDM_Triggered_Reset reqd «signal» Data_Update_Stop</div></div></div></div></div></div>			





ID	Type	Requirement Part 1	Requirement Part 2	JIRA	V 4.0 (6.A) > V 4.0 (4.A)
Eu.Gen.4154	Def	<div><div>[Block] EULYNX field element Subsystem [Functional Viewpoint - Subsystem Requirements - Functional Architecture]</div><div><div>ibd [Block] EULYNX field element Subsystem [Functional Viewpoint - Subsystem Requirements - Functional Architecture]</div><div><div>«logical structural entity» EULYNX field element Subsystem</div><div><div><div>SMI_XX</div><div><div>T1in_Maintaining_finished</div><div>T2in_Preload_item_i_started</div><div>T3in_Preload_item_i_finished</div><div>T4in_Activating_item_i</div><div>T5in_Data_installation_item_i_finished</div><div>T6in_Update_process_aborted</div><div>T10out_Operation_State</div><div>d52in_EST_EfeS_init_SubState : String</div><div>D10out_State</div><div>T9out_Activation_State</div><div>D26in_Con_tmax_DataInstallation</div><div>D9out_State : F_SMI_EfeS</div><div>T8out_Preload_State</div><div>D28in_Direct_Reboot_necessary</div><div>D8out_State</div><div>T17out_Start_SMI_Connection</div><div>T19in_Start_async_preload</div><div>T23in_SMI_Connection_closed</div><div>T30in_MDM_Request_Reset</div><div>D2in_Con_tmax_Response_MDM</div><div>T32in_Registrations_Ready</div><div>D3in_Con_tmax_SMI_Connection</div><div>T33out_Initialisation_done</div><div>D22in_item_i_activation_readiness</div><div>T36in_MDM_Safe_Maintenance</div><div>D25in_Con_tmax_DataTransmission</div></div><div><div>D21in_Con_tmax_No_Maintenance : Integer</div><div>p2inout : ~EST_SCI_GEN</div><div>p3inout : EST_SMI_GEN</div><div>p2inout : EST_SCI_GEN</div><div>«functional entity» EST EfeS : F_EST_EfeS</div><div>d51out_EST_EfeS_State</div><div>T1in_Power_On_Detected</div><div>T2in_Power_Off_Detected</div><div>T3in_Reset</div><div>T4in_Booted</div><div>T5in_SIL_Not_Fulfilled</div><div>T7in_Invalid_Or_Missing_Basic_Data</div><div>D20in_Con_MDM_Used</div></div><div><div>P1inout : SCI_GEN</div><div>P1inout : SCI_GEN</div><div>p3inout : ~F_SCI_Specific</div><div>D3in_Con_PDI_Version</div><div>D4in_Con_Checksum_Data</div><div>d50out_PDI_Connection_State</div><div>«functional entity» : F_SCI_EfeS_Sec</div><div>T10in_SCP_Connection_Terminated</div><div>T12out_Terminate_SCP_Connection</div><div>T20in_Protocol_Error</div><div>T21in_Formal_Telegram_Error</div><div>T22in_Content_Telegram_Error</div><div>T5in_SCP_Connection_Established</div></div></div></div></div></div></div>			
Eu.Gen.4155	Def	EfeSX			
Eu.Gen.4156	Def	SMI_XX			
Eu.Gen.3623	Head	3.1.2.4 EULYNX field element Subsystem - Functional Entities			
Eu.Gen.3288	Info	F_EST_EfeS			
Eu.Gen.3333	Req	<div><div>[Block] F_EST_EfeS [Functional Viewpoint - Subsystem Requirements - Functional Entity]</div><div><div>ibd [Block] F_EST_EfeS [Functional Viewpoint - Subsystem Requirements - Functional Entity]</div><div><div>«functional entity» F_EST_EfeS</div><div><div>Operation</div><div>«Operation» cOp1_init ()</div><div><div>T1in_Power_On_Detected : PulsedIn</div><div>T2in_Power_Off_Detected : PulsedIn</div><div>T3in_Reset : PulsedIn</div><div>T4in_Booted : PulsedIn</div><div>T5in_SIL_Not_Fulfilled : PulsedIn</div><div>T7in_Invalid_Or_Missing_Basic_Data : PulsedIn</div><div>D20in_Con_MDM_Used : Boolean</div><div>D21in_Con_tmax_No_Maintenance : Integer</div><div>p2inout : ~EST_SCI_GEN</div><div>p3inout : EST_SMI_GEN</div><div>d51out_EST_EfeS_State : String</div><div>d52out_EST_EfeS_init_SubState : String</div></div></div></div></div></div>			
Eu.Gen.3289	Def	<div>/* cOp1_init */  d52out_EST_EfeS_init_SubState := "unknown";</div>	cOp1_init		



ID	Type	Requirement Part 1	Requirement Part 2	JIRA	V 4.0 (6.A) > V 4.0 (4.A)
Eu.Gen.3341	Def	T1in_Power_On_Detected	The port T1in_Power_On_Detected indicates that the voltage reaches the permitted range for operation.		
Eu.Gen.3344	Def	T2in_Power_Off_Detected	The port T2in_Power_Off_Detected indicates that voltage leaves the permitted range for operation.		
Eu.Gen.3345	Def	T3in_Reset	The port T3in_Reset represents the receiving of the request Local_Reset from Maintainer.		
Eu.Gen.3346	Def	T4in_Booted	<p>The port T4in_Booted indicates that the booting process was completed successfully.</p> <p>T4in_Booted shall be trigged under the following conditions:</p> <ul style="list-style-type: none"><li>- all conditions for the required Safety Integrity Level are fulfilled</li><li>- the basic data is valid and complete</li><li>- the safe communication protocol connection is disconnected</li><li>- the time synchronisation was initiated</li></ul>		
Eu.Gen.3347	Def	T5in_SIL_Not_Fulfilled	The port T5in_SIL_Not_Fulfilled indicates that at least one condition for the needed Safety Integrity Level is not fulfilled.		
Eu.Gen.3348	Def	T7in_Invalid_Or_Missing_Basic_Data	The port T7in_Invalid_Or_Missing_Basic_Data indicates a failed validity check of Basic Data on the system data interface to Basic Data identifier.		
Eu.Gen.3290	Def	D20in_Con_MDM_Used	<p>The port D20in_Con_MDM_Used provides configuration values whether the MDM is used during initialisation.</p> <p>The following values are permitted:</p> <ul style="list-style-type: none"><li>- true: MDM is used</li><li>- false: MDM is not used</li></ul>		
Eu.Gen.4230	Def	D21in_Con_tmax_No_Maintenance	The port D21in_Con_tmax_No_Maintenance refines the time value Con_tmax_No_Maintenance.		
Eu.Gen.3292	Def	d51out_EST_EfeS_State	The port d51out_EST_EfeS_State indicates the current essential state of the EULYNX field element Subsystem. This Operation_State is displayed at the local status display for the Maintainer.		
Eu.Gen.4171	Def	d52out_EST_EfeS_init_SubState	The port d52out_EST_EfeS_init_SubState indicates the current substate within the essential state INITIALISING of the EULYNX field element Subsystem.		
Eu.Gen.4195	Def	p2inout			
Eu.Gen.4196	Def	p3inout			
Eu.Gen.3293	Info	F_EST_EfeS - Behaviour			

ID	Type	Requirement Part 1	Requirement Part 2	JIRA	V 4.0 (6.A) > V 4.0 (4.A)
Eu.Gen.3294	Req	<div>Functional Viewpoint - Subsystem Requirements - Functional Entity</div> <div>stm [State Machine] F_EST_EfeS - Behaviour [Functional Viewpoint - Subsystem Requirements - Functional Entity]</div> <div><p>The diagram illustrates the state machine for F_EST_EfeS - Behaviour. It starts with an initial state 'Initial0' leading to 'NO_OPERATING_VOLTAGE'. A transition 'when( T1in_Power_On_Detected ) /' leads to 'OPERATING_VOLTAGE_SUPPLIED'. From there, 'Initial1' leads to 'BOOTING'. 'BOOTING' has several transitions: 'when( T3in_Reset ) /' leads to 'FALLBACK_MODE'; 'when( T5in_SIL_Not_Fulfilled ) /' leads to 'FALLBACK_MODE'; 'when( T7in_Invalid_Or_Missing_Basic_Data ) /' leads to 'FALLBACK_MODE'; 'MDM_Triggered_Reset /' leads to 'FALLBACK_MODE'; 'when( T3in_Reset ) /' leads to 'FALLBACK_MODE'; 'when( T4in_Booted ) /' leads to 'INITIALISING'. 'INITIALISING' leads to 'WAITING_FOR_PDI_OR_MAINTENANCE' via a junction 'Junction0' and guard '[D20in_Con_MDM_Used] /'. 'WAITING_FOR_PDI_OR_MAINTENANCE' has transitions: '[NOT D20in_Con_MDM_Used] /' leads to 'WAITING_FOR_PDI'; 'Data_Update_Finished /' leads to 'WAITING_FOR_PDI_OR_MAINTENANCE'; 'MDM_Commanded_Maintenance [D20in_Con_MDM_Used] /' leads to 'WAITING_FOR_NO_MAINTENANCE_TIMEOUT'. 'WAITING_FOR_PDI' has transitions: 'PDI_Connection_Closed /' leads to 'WAITING_FOR_PDI_OR_MAINTENANCE'; 'PDI_Connection_Started /' leads to 'WAITING_FOR_PDI_OR_MAINTENANCE'; 'PDI_Connection_Closed /' leads to 'WAITING_FOR_PDI'; 'PDI_Released_For_Maintenance [NOT D20in_Con_MDM_Used] /' leads to 'WAITING_FOR_NO_MAINTENANCE_TIMEOUT'; 'PDI_Released_For_Maintenance [D20in_Con_MDM_Used] /' leads to 'WAITING_FOR_DATA_UPDATE'. 'WAITING_FOR_NO_MAINTENANCE_TIMEOUT' has transitions: 'after( D21in_Con_tmax_No_Maintenance ) /' leads to 'WAITING_FOR_PDI_OR_MAINTENANCE'; 'PDI_Released_For_Maintenance [NOT D20in_Con_MDM_Used] /' leads to 'WAITING_FOR_DATA_UPDATE'. 'WAITING_FOR_DATA_UPDATE' has transitions: 'Entry/send EST_Ready_For_Maintenance to p3inout; send NotReady_For_PDI_Connection to p2inout; d52out_EST_EfeS_init_SubState := "WAITING_FOR_DATA_UPDATE";' leads to 'WAITING_FOR_DATA_UPDATE'; 'PDI_Released_For_Maintenance [D20in_Con_MDM_Used] /' leads to 'WAITING_FOR_PDI'; 'PDI_Connection_Established /' leads to 'OPERATIONAL'. 'OPERATIONAL' has transitions: 'when( T3in_Reset ) /' leads to 'FALLBACK_MODE'; 'when( T5in_SIL_Not_Fulfilled ) /' leads to 'FALLBACK_MODE'. 'FALLBACK_MODE' has transitions: 'when( T5in_SIL_Not_Fulfilled ) /' leads to 'FALLBACK_MODE'; 'when( T7in_Invalid_Or_Missing_Basic_Data ) /' leads to 'FALLBACK_MODE'; 'MDM_Triggered_Reset /' leads to 'FALLBACK_MODE'; 'when( T3in_Reset ) /' leads to 'FALLBACK_MODE'; 'when( T4in_Booted ) /' leads to 'INITIALISING'. 'NO_OPERATING_VOLTAGE' has transitions: 'when( T2in_Power_Off_Detected ) /' leads to 'FALLBACK_MODE'; 'send NotReady_For_PDI_Connection to p2inout; send Data_Update_Stop to p3inout;' leads to 'FALLBACK_MODE'.</p></div>	<p>This state machine diagram describes the requirements for the following functionalities:</p> <ul style="list-style-type: none"><li>- basic behaviour for each EfeS based on "external/internal triggers" like<ul style="list-style-type: none"><li>* establishment and closure of PDI connection</li><li>* loss of SIL</li><li>* Reset of the EfeS</li></ul></li></ul> <p>Note: The state machine describes the lifecycle of an EfeS through its operational states.</p>		

This state machine diagram describes the requirements for the following functionalities:

- basic behaviour for each EfeS based on "external/internal triggers" like
  - \* establishment and closure of PDI connection
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Note: The state machine describes the lifecycle of an EfeS through its operational states.

ID	Type	Requirement Part 1	Requirement Part 2	JIRA	V 4.0 (6.A) > V 4.0 (4.A)
Eu.Gen.3295	Def	Initial0			
Eu.Gen.3296	Def	/cOp1_init();{Initial0 - NO_OPERATING_VOLTAGE}			
Eu.Gen.3297	Def	NO_OPERATING_VOLTAGE			
Eu.Gen.3299	Def	when(T1in_Power_On_Detected)/{NO_OPERATING_VOLTAGE - OPERATING_VOLTAGE_SUPPLIED}			
Eu.Gen.4172	Def	entry/d51out_EST_EfeS_State := "NO_OPERATING_VOLTAGE";{State-internal in NO_OPERATING_VOLTAGE}			
Eu.Gen.3300	Def	OPERATING_VOLTAGE_SUPPLIED			
Eu.Gen.3301	Def	BOOTING			
Eu.Gen.3304	Def	when(T4in_Booted)/{BOOTING - INITIALISING}			
Eu.Gen.3305	Def	when(T5in_SIL_Not_Fulfilled)/{BOOTING - FALLBACK_MODE}			
Eu.Gen.3306	Def	when(T7in_Invalid_Or_Missing_Basic_Data)/{BOOTING - FALLBACK_MODE}			
Eu.Gen.4173	Def	entry/d51out_EST_EfeS_State := "BOOTING";{State-internal in BOOTING}			
Eu.Gen.3307	Def	FALLBACK_MODE			
Eu.Gen.3309	Def	when(T3in_Reset)/{FALLBACK_MODE - BOOTING}			
Eu.Gen.4174	Def	MDM_Triggered_Reset/{FALLBACK_MODE - BOOTING}			
Eu.Gen.4175	Def	entry/d51out_EST_EfeS_State := "FALLBACK_MODE";{State-internal in FALLBACK_MODE}			
Eu.Gen.3310	Def	Initial1			
Eu.Gen.3311	Def	/{Initial1 - BOOTING}			
Eu.Gen.3327	Def	OPERATIONAL			
Eu.Gen.3330	Def	when(T3in_Reset)/ send NotReady_For_PDI_Connection to p2inout;{OPERATIONAL - BOOTING}			
Eu.Gen.3331	Def	when(T5in_SIL_Not_Fulfilled)/ send NotReady_For_PDI_Connection to p2inout;{OPERATIONAL - FALLBACK_MODE}			
Eu.Gen.3325	Def	PDI_Connection_Closed/{OPERATIONAL - WAITING_FOR_PDI_OR_MAINTENANCE}			
Eu.Gen.4194	Def	entry/d51out_EST_EfeS_State := "OPERATIONAL";{State-internal in OPERATIONAL}			
Eu.Gen.4229	Def	PDI_Released_For_Maintenance[D20in_Con_MDM_Used]/{OPERATIONAL - WAITING_FOR_DATA_UPDATE}			
Eu.Gen.4235	Def	PDI_Released_For_Maintenance[NOT D20in_Con_MDM_Used]/{OPERATIONAL - WAITING_FOR_NO_MAINTENANCE_TIMEOUT}			
Eu.Gen.3332	Def	when(T2in_Power_Off_Detected)/ send NotReady_For_PDI_Connection to p2inout; send Data_Update_Stop to p3inout;{OPERATING_VOLTAGE_SUPPLIED - NO_OPERATING_VOLTAGE}			
Eu.Gen.4176	Def	INITIALISING			
Eu.Gen.4177	Def	Initial2			
Eu.Gen.4178	Def	/{Initial2 - Junction0}			
Eu.Gen.4179	Def	Reboot_Required[D20in_Con_MDM_Used]/ send NotReady_For_PDI_Connection to p2inout;{INITIALISING - BOOTING}			
Eu.Gen.4180	Def	entry/d51out_EST_EfeS_State := "INITIALISING";{State-internal in INITIALISING}			
Eu.Gen.4181	Def	WAITING_FOR_DATA_UPDATE			
Eu.Gen.4182	Def	Data_Update_Finished/ send Ready_For_PDI_Connection to p2inout;{WAITING_FOR_DATA_UPDATE - WAITING_FOR_PDI_OR_MAINTENANCE}			
Eu.Gen.4183	Def	entry/send EST_Ready_For_Maintenance to p3inout; send NotReady_For_PDI_Connection to p2inout; d52out_EST_EfeS_init_SubState := "WAITING_FOR_DATA_UPDATE";{State-internal in WAITING_FOR_DATA_UPDATE}			
Eu.Gen.4184	Def	WAITING_FOR_PDI			
Eu.Gen.4185	Def	PDI_Connection_Closed/{WAITING_FOR_PDI - WAITING_FOR_PDI_OR_MAINTENANCE}			
Eu.Gen.3320	Def	PDI_Connection_Established/{WAITING_FOR_PDI - OPERATIONAL}			
Eu.Gen.4186	Def	PDI_Released_For_Maintenance[D20in_Con_MDM_Used]/{WAITING_FOR_PDI - WAITING_FOR_DATA_UPDATE}			
Eu.Gen.4187	Def	entry/d52out_EST_EfeS_init_SubState := "WAITING_FOR_PDI";{State-internal in WAITING_FOR_PDI}			
Eu.Gen.4234	Def	PDI_Released_For_Maintenance[NOT D20in_Con_MDM_Used]/{WAITING_FOR_PDI - WAITING_FOR_NO_MAINTENANCE_TIMEOUT}			
Eu.Gen.4188	Def	WAITING_FOR_PDI_OR_MAINTENANCE			
Eu.Gen.4189	Def	MDM_Commanded_Maintenance[D20in_Con_MDM_Used]/{WAITING_FOR_PDI_OR_MAINTENANCE - WAITING_FOR_DATA_UPDATE}			
Eu.Gen.4190	Def	PDI_Connection_Started/{WAITING_FOR_PDI_OR_MAINTENANCE - WAITING_FOR_PDI}			
Eu.Gen.4191	Def	entry/d52out_EST_EfeS_init_SubState := "WAITING_FOR_PDI_OR_MAINTENANCE";{State-internal in WAITING_FOR_PDI_OR_MAINTENANCE}			
Eu.Gen.3318	Def	when(T3in_Reset)/ send NotReady_For_PDI_Connection to p2inout; send Data_Update_Stop to p3inout;{INITIALISING - BOOTING}			

ID	Type	Requirement Part 1	Requirement Part 2	JIRA	V 4.0 (6.A) > V 4.0 (4.A)
Eu.Gen.3319	Def	when(T5in_SIL_Not_Fulfilled)/ send NotReady_For_PDI_Connection to p2inout; send Data_Update_Stop to p3inout;{INITIALISING - FALLBACK_MODE}			
Eu.Gen.3321	Def	Junction0			
Eu.Gen.3322	Def	[NOT D20in_Con_MDM_Used]/ send Ready_For_PDI_Connection to p2inout;{Junction0 - WAITING_FOR_PDI_OR_MAINTENANCE}			
Eu.Gen.3323	Def	[D20in_Con_MDM_Used]/{Junction0 - WAITING_FOR_DATA_UPDATE}			
Eu.Gen.4231	Def	WAITING_FOR_NO_MAINTENANCE_TIMEOUT			
Eu.Gen.4232	Def	after(D21in_Con_tmax_No_Maintenance)/ send Ready_For_PDI_Connection to p2inout;{WAITING_FOR_NO_MAINTENANCE_TIMEOUT - WAITING_FOR_PDI_OR_MAINTENANCE}			
Eu.Gen.4233	Def	entry/d52out_EST_EfeS_init_SubState := "WAITING_FOR_NO_MAINTENANCE_TIMEOUT"; send NotReady_For_PDI_Connection to p2inout;{State-internal in WAITING_FOR_NO_MAINTENANCE_TIMEOUT}			
Eu.Gen.4215	Head	<b>3.1.3 EULYNX field element Subsystem - Interfaces</b>			
Eu.Gen.115	Head	<b>3.1.3.1 Interface to Basic Data Identifier</b>			
Eu.Gen.114	Head	<b>3.1.3.1.1 Basic Data Identifier Interface - Information Flows</b>			
Eu.Gen.116	Info	Basic_Data_Identifier	Definition of the InformationFlow (by FlowSpecification) for the interface to <u>Basic Data identifier</u> .		
Eu.Gen.117	Def	Basic_Data	The Basic_Data are the basis for booting the EULYNX field element Subsystem. The Basic_Data enables the EULYNX field element Subsystem to become operational.		
Eu.Gen.3613	Head	<b>3.1.3.2 Interface to Maintainer</b>			
Eu.Gen.3614	Head	<b>3.1.3.2.1 Maintainer Interface - Information Flows</b>			
Eu.Gen.3615	Info	Maintainer			
Eu.Gen.3616	Def	Ethernet_Connection_ETH1	Display of the status of the Ethernet_Connection_ETH1 of the EULYNX field element Subsystem at the local status display.		
Eu.Gen.3617	Def	Ethernet_Connection_ETH2	Display of the status of the Ethernet_Connection_ETH2 of the EULYNX field element Subsystem at the local status display.		
Eu.Gen.3618	Def	Local_Reset	Local request to reset the EULYNX field element Subsystem.		
Eu.Gen.3619	Def	Operation_State	Display of the status of the operation state of the EULYNX field element Subsystem at the local status display.		
Eu.Gen.3886	Head	<b>4 RAMSS requirements</b>			
Eu.Gen.3888	Info	The requirements for reliability, availability, maintainability, safety and security shall be defined by national specifications. Note: In future phases of the System Pillar, national specifications will be replaced by harmonised specifications.			
Eu.Gen.3887	Head	<b>5 Technical requirements</b>			
Eu.Gen.3889	Info	Additional technical requirements shall be defined by national specifications. Note: In future phases of the System Pillar, national specifications will be replaced by harmonised specifications.			
Eu.Gen.3890	Head	<b>5.1 Generic technical interface requirements</b>			
Eu.Gen.3891	Head	<b>5.1.1 Interface to the Point of Service – Signalling (PoS-Signalling)</b>			
Eu.Gen.3892	Req	The EULYNX field element Subsystem shall meet the requirements of the PoS-Signalling as defined in [Eu.Doc.100].			
Eu.Gen.3904	Head	<b>5.1.1.1 QoS</b>			
Eu.Gen.3905	Req	The EULYNX field element Subsystem shall meet or expect the QoS requirements as defined in Eu.PoS.1 (see [Eu.Doc.100]).			
Eu.Gen.3913	Head	<b>5.1.2 Interface to the Point of Power – Output (PoP-O)</b>			
Eu.Gen.3914	Info	These requirements shall be defined by national specifications. Note: In future phases of the System Pillar, national specifications will be replaced by harmonised specifications.			

ID	Type	Requirement Part 1	Requirement Part 2	JIRA	V 4.0 (6.A) > V 4.0 (4.A)
Eu.Gen.3915	Head	<b>5.1.3 Interface to Basic Data identifier</b>			
Eu.Gen.3918	Req	The basic data of the EULYNX field element subsystem shall be stored on a data carrier (Basic Data identifier).			
Eu.Gen.3919	Req	The Basic Data identifier shall be permanently assigned to the slot of the component of the EULYNX field element Subsystem (subsystem controller). To avoid erroneous assignments, the Basic Data identifier shall be attached to the subsystem in a manner that it remains assigned to that subsystem when a subsystem controller is replaced. Note: The implementation of the Basic Data identifier is left to the suppliers. This is not specified by EULYNX.			
Eu.Gen.3916	Head	<b>5.1.4 Local status display interface</b>			
Eu.Gen.3917	Info	These requirements shall be defined by national specifications. Note: In future phases of the System Pillar, national specifications will be replaced by harmonised specifications.			
Eu.Gen.3920	Head	<b>5.2 Electrical subsystem requirements</b>			
Eu.Gen.3921	Info	These requirements shall be defined by national specifications. Note: In future phases of the System Pillar, national specifications will be replaced by harmonised specifications.			
Eu.Gen.3922	Head	<b>5.3 Constructive subsystem requirements</b>			
Eu.Gen.3923	Info	These requirements shall be defined by national specifications. Note: In future phases of the System Pillar, national specifications will be replaced by harmonised specifications.			
Eu.Gen.3924	Head	<b>5.4 Configuration and engineering data</b>			
Eu.Gen.3925	Head	<b>5.4.1 Generic data</b>			
Eu.Gen.3926	Req	The configuration and engineering data for the EULYNX field element Subsystem shall as a minimum contain the information specified in Eu.SAS.1755.		EUAR-638	<b>Object Text:</b> The <del>minimum</del> configuration and engineering data for the EULYNX field element Subsystem <del>is</del> <u>shall as a minimum contain the information</u> specified in Eu.SAS.1755. <b>a_JIRA_BL4R3: EUAR-638</b>
Eu.Gen.3929	Req	The PDI connection from the EULYNX field element Subsystem to the Subsystem - Electronic Interlocking shall be assigned to exactly one safe communication by configuration.			
Eu.Gen.4260	Head	<b>5.4.2 Value configuration</b>			
Eu.Gen.4261	Req	<b>Con_tmax_No_Maintenance</b>  The time value shall be configured in accordance with:  Configurable resolution: steps of 1s Configurable range: from 1s up to 30s with a default value of 10s.  Con_tmax_No_Maintenance is defined in Eu.Gen.4250.  Note: A non-zero waiting time is needed to avoid fast looping in the interaction between EIL and EfeS.		EUAR-708	<b>Object Text:</b> Con_tmax_No_Maintenance  The time value shall be configured in accordance <del>to</del> <u>with</u> :  Configurable resolution: steps of 1s Configurable range: from 1s up to 30s with a default value of 10s.  Con_tmax_No_Maintenance is defined in Eu.Gen.4250.  Note: A non-zero waiting time is needed to avoid fast looping in the interaction between EIL and EfeS. <b>a_JIRA_BL4R3: EUAR-708</b>
Eu.Gen.4252	Head	<b>5.5 Time behaviour</b>			
Eu.Gen.4253	Info	All functions of the state "BOOTING" shall be completed by the subsystem within a defined time after the detection of the operating voltage at the Point of Power - Output (PoP-O). This time shall be defined by national specifications. Note: In future phases of the System Pillar, national specifications will be replaced by harmonised specifications.			